

Amendments to the Claims

The following listing of claims replaces all previous listings and versions of claims in this application.

1. (Currently amended) A method of thinning a wafer made of semiconductor material, the wafer having a first face supporting at least one electronic component or circuit and an opposing second face, which comprises:

implanting atomic species through the second face and into the wafer to obtain a zone of weakness at a predetermined depth therein, the zone defining a first portion of the wafer extending from the zone to the first face and a remaining portion constituted by the remaining portion of the wafer;

applying a stiffener to the second face of the wafer prior to removing the remaining portion;

removing the stiffener and the remaining portion from the first portion along the zone of weakness to thin the wafer, with the electronic component of circuit remaining on the first face, to provide a new second face opposite the first face on the first portion; and

repeating the implanting and removing steps until the first portion has a reduced thickness for constituting a self-supported thin layer for the electronic component or circuit supported on the first face.

2. (Original) The method of claim 1, which further comprises thinning the wafer by a mechanical or chemical thinning method prior to the implanting of the atomic species.

3. (Previously presented) The method of claim 1, which further comprises providing the at least one electronic component or circuit on the first face of the wafer prior to the implanting of the atomic species, with the thin layer supporting the at least one electronic component.

4. (Original) The method of claim 1, wherein the remaining portion of the wafer is removed by applying a heat treatment or an external mechanical stress.

5. (Original) The method of claim 1, wherein the remaining portion of the wafer is removed by blowing a jet of fluid adjacent the zone of weakness.
6. (Cancelled)
7. (Cancelled)
8. (Previously presented) The method of claim 1, which further comprises applying the stiffener by deposition.
9. (Original) The method of claim 8, wherein the stiffener comprises a layer of silicon oxide.
10. (Previously presented) The method of claim 1, wherein the stiffener comprises a rigid plate.
11. (Original) The method of claim 10, wherein the rigid plate comprises a monocrystalline or polycrystalline silicon material or a glass.
12. (Previously presented) The method of claim 1, wherein the stiffener comprises a flexible film.
13. (Previously presented) The method of claim 1, wherein the stiffener comprises an adhesive film.
14. (Previously presented) The method of claim 1, wherein the stiffener comprises a layer of wax.
15. (Cancelled)

16. (Original) The method of claim 1, wherein the wafer comprises silicon.

17. (Original) The method of claim 1, wherein the wafer comprises a silicon on insulator wafer.

18. (Original) The method of claim 1, wherein the wafer comprises germanium, an alloy of silicon and germanium, silicon carbide, gallium arsenide, indium phosphide, gallium nitride or aluminum nitride.

19. (Currently amended) A method of thinning a wafer made of semiconductor material, the wafer having first and second opposing faces, which comprises:
providing at least one electronic component or circuit on the first face of the wafer;

implanting atomic species through the second face and into the wafer to obtain a zone of weakness at a predetermined depth therein, the zone defining a first portion of the wafer extending from the zone to the first face and a remaining portion constituted by the remaining portion of the wafer;

applying a stiffener to the second face of the wafer prior to removing the remaining portion;

removing the stiffener and the remaining portion from the first portion along the zone of weakness to thin the wafer, with the electronic component or circuit remaining on the first face; and

if necessary, repeating the implanting and removing steps until the first portion has a reduced thickness for constituting a self-supported thin layer for the electronic component or circuit.

20. (Previously presented) A method of thinning a wafer made of semiconductor material, the wafer having a first face supporting at least one electronic component or circuit and an opposing second face, which comprises:

implanting atomic species through the second face and into the wafer to obtain a zone of weakness at a predetermined depth therein, the zone defining a first portion of the wafer

extending from the zone to the first face and a remaining portion constituted by the remaining portion of the wafer;

removing the remaining portion from the first portion along the zone of weakness to thin the wafer;

applying a stiffener to the second face of the wafer prior to removing the remaining portion; and

repeating the implanting, applying and removing steps until the first portion has a reduced thickness that corresponds to a desired thickness of less than 35 μm for constituting a self-supporting thin layer with the electronic component or circuit remaining supported on the first face.

21. (Previously presented) The method of claim 1, wherein the reduced thickness is less than 35 μm .

22. (Previously presented) The method of claim 1, wherein the zone of weakness defines the remaining portion extending therefrom to the second face.

23. (New) The method of claim 19, wherein the first portion comprises a self-supported portion between the electronic component or circuit and the zone of weakness, and the electronic component or circuit is supported on the self-supported portion during and after the removal of the stiffener.

24. (New) The method of claim 23, wherein the electronic component or circuit has opposing first and second sides, with the second side being supported on the self-supported portion during and after the removal of the stiffener.